Docket No.: 1999P1827

I hereby certify that this correspondence in the Assistant Commissioner for Patents, Washington, D.C. 2023 Lon the date indicated below.

ву:

Date: March 11, 2002

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

Otto Schneider et al.

Applic. No.

10/021,689

Filed

November 13, 2001

Technology Center 2100

SEP 1 8 2002

RECEIVED

Title

Circuit Configuration for Generating Current Pulses in the Supply

Current of Integrated Circuits

Art Unit

2816

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

are submitted herewith:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications

United States Patent No. 4,797,579 (Lewis), dated January 10, 1989;

United States Patent No. 4,829,199 (Prater), dated May 9, 1989;

United States Patent No. 5,041,741 (Steele), dated August 20, 1991;

United States Patent No. 5,598,348 (Rusu et al.), dated January 28, 1997

United States Patent No. 5,625,803 (McNelly et al.), dated April 29, 1997;

United States Patent No. 5,768,145 (Roethig), dated June 16, 1998;

RECEIVED : TECHNOLOGY CENTER ?

J.

European Patent Application EP 0 544 224 A2 (Kinugasa et al.), dated June 2, 1993;

Patent Database Search Results 1976-1999, dated January 21, 1999.

Respectfully submitted,

For Applicants

Date: March 11, 2002

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE PATENT AND TRADEMARK OFFICE PATENT AND TRADEMARK OFFICE AND TRADEMARK OFFICE OFFICE OFFIC				Attorney Docket No.: Applic. No. 1999P1827 10/021,689 Applicant Otto Schneider et al. Filing Date Group Art Unit November 13, 2001 2816													
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